

1036 U.S. PTO

10/034221



12/28/01

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U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10034221	FILING DATE 12/28/2001	CLASS 438	SUBCLASS 94	GAU 2812	EXAMINER Anderson
<b>**APPLICANTS:</b> Pyi Seung; 117 1765 <div style="text-align: center; font-size: 2em; font-weight: bold;">ABANDONED</div>					
<b>**CONTINUING DATA VERIFIED:</b>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> REPUBLIC OF KOREA 2001-67090 10/30/2001					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Free in priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO			
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		CU-2755 VE			
Verified and Acknowledged Examiners's initials					
<b>TITLE :</b> Method for fabricating a semiconductor epitaxial wafer having doped carbon and a semiconductor epitaxial wafer					
U.S. DEPT. OF COMM./PAT & TM-PTO-436L (Rev. 12-94)					

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<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
		Application Examiner	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	
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